

## Active

- L1: (5336) antifuse or anti-fuse or (anti adj fuse)
- L2: (15698) dielectric adj2 (breakdown or break-down or (break adj down))
- L3: (3457) defect\$5 adj2 address
- L4: (154) 1 with 2
- L7: (13) 3 and 4
- L9: (78) 1 same 3
- L10: (65) 9 not 7
- L14: (2986) redundant\$3 adj control\$4
- L16: (6666) program\$6 adj element
- L16: (55) 3 same 15
- L18: (58) 14 and 16
- L19: (169) 3 and 16
- L20: (103) 19 not (16 or 18)
- L21: (1532) 365/226.7
- L22: (6619) 365/200
- L23: (1223) 365/96
- L24: (628) 327/626
- L25: (216) 327/626
- L26: (178) 1 and 3
- L27: (240) 20 or 26
- L28: (57) 27 and 21
- L29: (133) 27 and 22
- L30: (26) 27 and 23
- L31: (7) 27 and 24
- L32: (7) 27 and 25
- L33: (27) (FUJIMA near2 SHIRO).in.

## Failed

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DB: US-PCPUB;USPAT;EPO;IPO;DERWENT;IBM\_108

Default operator: OR

Back

Highlight all 14 terms initially

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	U	C	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6115283 A	20000905	36	Semiconductor device with programming capacitance, claim	366/149	365/200		Hidaka; Hideto	<input checked="" type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6438059 B2	20020820	16	Fuse programming circuit for programming fuses	366/226.7	257/E27.062; 327/526; 365/226.7; 366/98		Akita; Hironobu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6163488 A	20001219	31	Semiconductor device with antifuse	366/200	365/226.7; 366/185.09; 366/186.26		Tanizaki; Hiroaki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6424584 B1	20020723	16	Redundancy antifuse bank for a memory device	366/226.7	366/185.09; 366/186.26		Seyyedy; Mirmajid	<input checked="" type="checkbox"/>	<input type="checkbox"/>

Hits: 0 Details HTML

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